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Substitute for form 1449PPTO

Complete If Known

Application Number 09/808,162
Filing Date March 13, 2001
First Named Inventor Yamazaki et al
Art Unit 2812
Examiner Name V. Simkovic
Attorney Docket Number ISEL 139 DIV

INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Use as many sheets as necessary)

Sheet 1 of 12

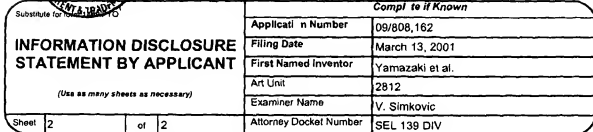
U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.†	Document Number <small>Number, Kind Code, Date, etc.</small>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
<i>[Handwritten initials]</i>	US-	6,371,037	12/06/1994	Yonehara	
	US-	6,374,564	12/20/1994	Bruehl	
	US-	5,714,395	02/03/1998	Bruehl	
	US-	5,985,681	11/16/1999	Hamajima et al.	
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FOREIGN PATENT DOCUMENTS					
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LIST OF PUBLICATIONS CITED BY APPLICANT	Attorney Docket No. SEL 139 DIV	Serial No. Not Assigned
	Applicant Shunpei YAMAZAKI et al	
	Filing Date Herewith	Group

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*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
LS	4,933,298	06/1990	Hasegawa	437	62	
	5,643,826	07/01/97	Ohtani et al	437	88	10/25/94
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	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE

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LS	1) Auberton-Herve, A.J. et al, "Industrial Research Society (Kogyo Chosa Kai)," Electronic Material, pp. 83-87, August, 1977.
	2) Izumi, K. et al, "C.M.O.S. Devices Fabricated on Buried SiO ₂ Layer Formed by Oxygen Implantation into Silicon," Electronics Letters, vol. 14, no. 18, pp. 593-597, August 31, 1978.
	3) Sakaguchi, K. et al, "Current Progress in Epitaxial Layer Transfer (ELITRAN)," IEICE Trans. Electron, vol. E80 C, no. 3, pp. 378-387, March, 1997.

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